

L Number	Hits	Search Text	DB	Time stamp
1	24	(((((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/09 12:09
2	28	(((((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with (array bank block))) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/09 12:55
-	395	(timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:23
-	340	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:23
-	225	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:27
-	5	(((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address))) and (low adj2 isolation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:27
-	166	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:29
-	3	(((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 signal))) and (low adj2 isolation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:29
-	138	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:35
-	3	(((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe))) and (low adj2 isolation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/05 18:48
-	0	(input with (isolation adj2 signal)) and (output with (address adj2 decoder)) and ((timing adj2 circuit) same (input adj2 signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:32
-	44	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe)) and (and\$1 adj2 gate) and (or\$1 adj2 gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/05 17:30
-	1017	((digit or bit) adj2 line) and (sense adj2 (amp or amplifier)) and (isolat\$3 adj3 (gate or signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 20:20
-	6	("5970022" "4571705" "5132931" "5335206" "5410508" "5459846").pn.	USPAT	2002/08/06 12:05

-	6	((("5970022" "4571705" "5132931" "5335206" "5410508" "5459846").pn.) and (input isolat\$3 signal output address decod\$3 timing circuit activat\$3 initiat\$3 trigger\$3 RAS NOR\$3 AND\$3 wordline)	USPAT	2002/08/06 17:47
-	6	((timing adj2 circuit) same (input adj2 signal) same (isolat\$3 adj2 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:30
-	2	((timing adj2 circuit) same (address adj2 decoder)) and (input adj2 signal) and (isolat\$3 adj2 signal) and output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:33
-	3	((timing adj2 circuit) same (address adj2 decoder)) and input and (isolat\$3 adj2 signal) and output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:34
-	976	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:04
-	141	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:17
-	95	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:31

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139

340) ((timing add3 circuit) and input

1 (340) ((timing adj3 circuit) and inpu  
2 (325) ((timing adj2 circuit) and inpu

2 (225) (((timing adj3 circuit) and in

(5) (((timing adj3 circuit) and input

(166) ((timing adj3 circuit) and in)

(3) (((timing adj3 circuit) and inpu

(138) ((timing ed13 circuit) and in

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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DB: USPAT:US:PGPUB:EPO:JPO:DERWENT:BM:TDB

Default operator: GR

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(((((sense adj2 (amp amplifier)) with (shar$3 switch$3
select$3)) same (second with array)) and (timing adj3
circuit)) and ((address wordline) adj3 decoder)) and
(isolation or ISO)
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 BRS term
  IS&R term
  Image
  Text
  HTML

	Current OR	Current XRef	Retrieval C	Inventor	8	C	P	2	3	5	Image Doc	Dis	PT
5	714/763	714/42; 714/718		Watanabe, Naoya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6378102		<input type="checkbox"/>
6	365/222	365/201; 365/233		Hidaka, Hideto	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6366515		<input checked="" type="checkbox"/>
7	365/193	365/195; 365/196;		Park, Churoo et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6343036		<input type="checkbox"/>
8	711/169			Hsu, Fu-Chieh et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6295593		<input checked="" type="checkbox"/>
9	365/203	365/149; 365/226		Proebsting, Robert J.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6282135		<input type="checkbox"/>
10	713/601	711/167; 712/224;		Lee, Ho-Cheol	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6279116		<input type="checkbox"/>
11	365/191	365/194; 365/230.03;		Ooishi, Tsukasa	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6246614		<input type="checkbox"/>
12	365/208	365/207		Proebsting, Robert J.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6208575		<input type="checkbox"/>
13	711/122	711/105		Hsu, Fu-Chieh et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6128700		<input type="checkbox"/>
14	438/239	438/253		Lu, Nicky C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6107134		<input checked="" type="checkbox"/>








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